

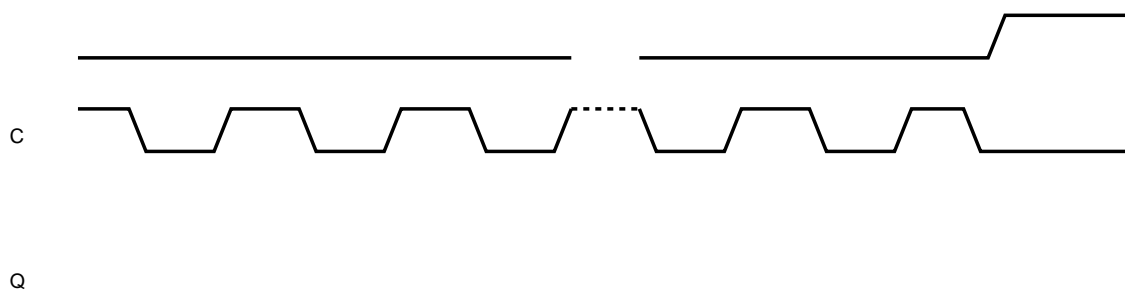




Figure 16. Serial input timing

Figure 17. Hold timing

Figure 18. Output timing



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